


ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

LOW VOLTAGE ELECTRON SOURCE WITH SELF ALIGNED
GATE APERTURES, FABRICATION METHOD THEREOF, AND
LUMINOUS DISPLAY USING THE ELECTRON SOURCE

Application Number : 10/707342 
Confirmation Number: 1341
First Named Applicant: Zhidan Tolt
Attorney Docket Number: nanogate120303
Art Unit: 2815
Examiner: Jesse A. Fenty
Search string: (6864162 or 6250984 or 6858455 or 6692324 or 6741019 or 6664727).pn

Certification: This Information Disclosure Statement was submitted under the following conditions, which satisfies the requirement under 37 CFR 1.97(e). The filer certified:

That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
BB	1	6864162	2005-03-08	Jin, Sungho		438	551
I	2	6250984	2001-06-26	Jin, Sungho, et al.		445	51
I	3	6858455	2005-02-22	Guillom et al.		438	20
↓	4	6692324	2004-02-17	Simpson et al.		445	24
BB	5	6741019	2004-05-25	Eilas, et al.		313	355
BB	6	6664727	2003-12-16	Nakamoto			

Signature

Examiner Name	Date
Paul Brada	9/12/2007